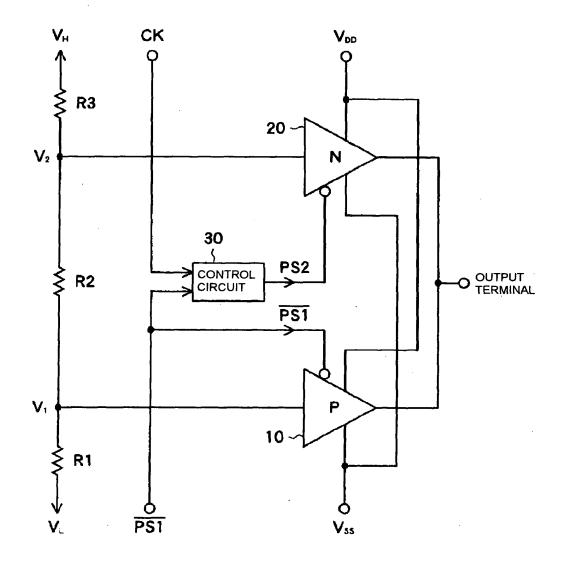
Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

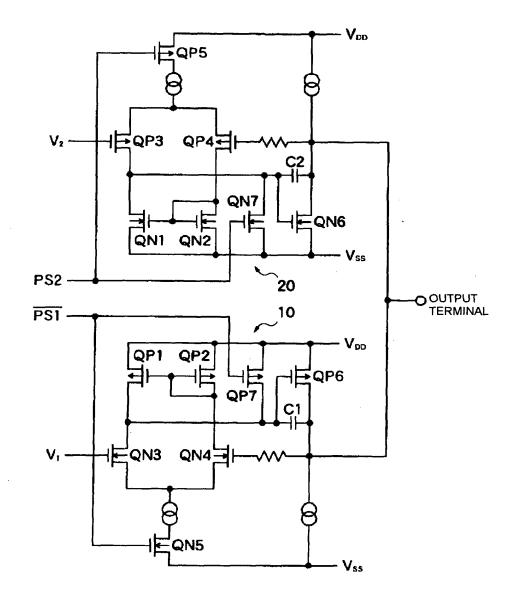
FIG.1



Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

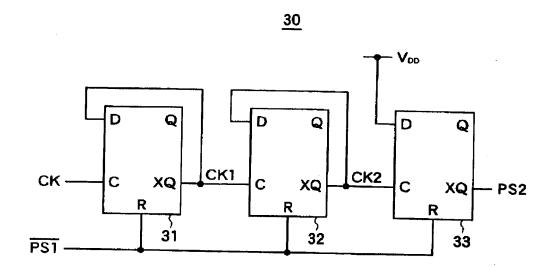
FIG.2



Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

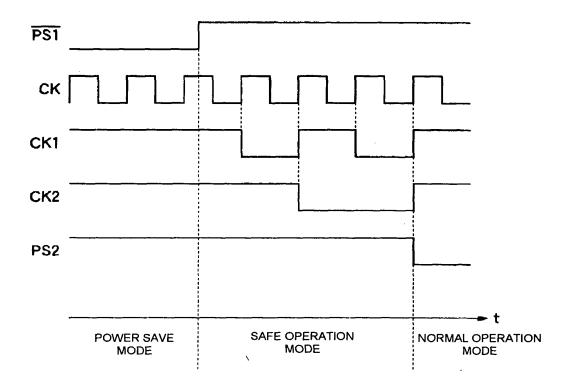
FIG.3



Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

FIG.4



Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

FIG.5



